



## **Developing an Automatic Switch for Home or Industrial Power Supply Changeover**

**Bamidele Onipede<sup>1\*</sup>, Samuel Joseph<sup>2</sup> and Omakoji Odiba<sup>2</sup>**

<sup>1</sup>University of Stuttgart, Germany.

<sup>2</sup>Prototype Engineering Development Institute, Ilesa, Osun State, Nigeria.

### **Authors' contributions**

*This work was carried out in collaboration between all authors. Author BO carried out the first designs which was further extended by author SJ and later fine tuned by all three authors. The first draft was put together by author SJ. Authors BO and OO managed the literature searches. The final manuscript was read and approved by all the authors.*

### **Article Information**

DOI: 10.9734/BJAST/2017/32785

#### Editor(s):

(1) Dr. Rodolfo Dufo Lopez, Electrical Engineering Department, University of Zaragoza, Spain.

#### Reviewers:

(1) Jose Alfredo Villagomez-Cortes, University of Veracruz, Mexico.

(2) Agbetuyi Ayoade Felix, Covenant University, Nigeria.

(3) Ezema Chukwuedozie Nnaemeka, Nnamdi Azikiwe University, Nigeria.

Complete Peer review History: <http://www.sciencedomain.org/review-history/19415>

**Original Research Article**

**Received 15<sup>th</sup> March 2017**

**Accepted 16<sup>th</sup> May 2017**

**Published 8<sup>th</sup> June 2017**

### **ABSTRACT**

Continued development of automatic change over switch is a necessary requirement for the principle of energy mix especially in developing countries characterised by insufficient mains power supply. In this paper, we report how we were able to intuitively use basic electronic components to implement an efficient automatic change over switch. In the event of a power failure from the mains supply, this automatic changeover is designed and constructed to switch on a standby generator and transfer the installation load to it (the generator) and vice versa when power is restored. The switching time was calculated and tested to be about 0.88 ms which is sufficient to prevent any noticeable disruption in the power supply to the installation load, whether home or industrial.

*Keywords: Automatic changeover switch; renewable energy source; standby power.*

\*Corresponding author: E-mail: [deleonipede@gmail.com](mailto:deleonipede@gmail.com);

## 1. INTRODUCTION

The modern approach to energy utility from global point of view is a drive from conventional to renewable energy, most especially fossil fuel. However this transformation cannot be total or immediate. It is a gradual process. In view of this, the principle of energy mix is practiced in modern energy utilization. In this regard, there is a mix of conventional and renewable energy based on availability. Today, solar energy, wind energy, and small hydro power are possible mix with conventional energy sources.

With the high dependence of home and industrial equipment on power, switching from one form of energy to the other has become a necessity either for integration in the national grid or for use in the standby home or industrial applications. Rocks and Mazur [1] describe a manual changeover in which the generator and load switching is done with physical influence. Certain mission critical appliances and installations which include hospitals, airports, high precision laboratory equipments which require a 100% up time cannot afford to depend on a manual changeover switch as described in [1]. The need for automatic electronic control which does the powering on of a standby generator and installation load switching in split seconds then cannot be overemphasized.

## 2. DESIGN

This circuit is modeled after an earlier work by our team [2]. With the aid of a voltage converter

as shown in Fig. 1, we obtain a d.c voltage from mains supply. The voltage drop across resistor  $R_2$  is compared with that in resistor  $R_3$ , a fixed reference via the help of the sensor unit. We expect that at a maximum, the mains voltage would experience a fluctuation down to a threshold of 190V a.c. We set the voltage drop at this voltage via  $R_2$  as reference. A D flip-flop switches the transistor OFF in reset mode from any voltage drop below the reference (A LOW signal) set by  $R_2$ . With the transistor switched OFF, the relay (RLA) becomes de-energized causing the contacts to close and switch over to the source of alternative supply which could be solar installations, wind, thermal, generator etc. When restoration of mains supply above the threshold voltage is detected by the voltage drop in  $R_2$  going higher than that in  $R_3$ , the D flip-flop receives a HIGH signal which energizes the relay by turning ON the transistor. By this, the installation (home/industrial load) is connected to the mains supply in SET mode via the normally open contact of RLA1.

The 4013-segment D-flip-flop logic device is included in the circuit to give a perfect switching and eliminate fluctuation that may occur as a result of voltage variation. This is included because voltage comparators will tend to switch the relays erratically [3]. Therefore, such effort as undertaken in this work can be a preamble to an elaborate or all- encompassing smart grid system for large energy applications or small scale industrial/domestic applications for smart and swift response among different sources of energy when deployed in energy mix bases.

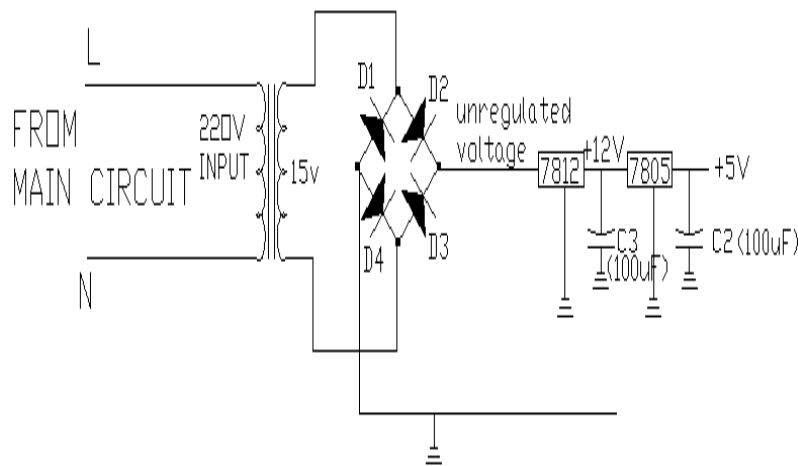


Fig. 1. Power unit

The changeover switch comprises of the following parts:

- Power Unit
- Comparator Unit
- Astable oscillator Unit
- Switching Unit

### 2.1 Power Unit

In Fig. 1, we show how the incoming a.c voltage is stepped down for rectification according to [3]. The transformation ratio,  $k$ , for a 220/15V step-down transformer used in our power unit is calculated from:

$$k = \frac{\text{Primary voltage}(V_p)}{\text{Secondary voltage}(V_s)} \quad (1)$$

The incoming a.c is converted to unregulated d.c by the bridge rectifier. These unregulated d.c voltage values can be obtained from:

$$V_{a.c(out)} = \frac{V_{a.c(in)}}{k} \quad (2)$$

Where,  $V_{a.c(in)}$  = input a.c voltage

$$V_{d.c}^+ = 0.9V_{a.c(out)} \quad (3)$$

Where,  $V_{d.c}^+$  = Unregulated d.c voltage

Voltage regulator LM 7805 delivers a regulated voltage of 5V d.c to the comparator, the 555 timer and the D flip-flop. Voltage regulator LM 7812 provides a regulated voltage of 12V d.c which is used in energizing the magnetic relay. Table 1 [2] shows the unregulated d.c voltages at different input a.c voltages.

### 2.2 Comparator Unit

A comparator compares two voltage signals and determines which signal is bigger and gives an output [4]. In our design the comparator unit determines when there's a disparity between the mains supply which is converted to d.c by the power supply unit and the preset reference voltage set by  $R_2$ . In Fig. 2 is shown the connection of the LM339 serving to compare the fixed reference input to the unregulated input  $R_2$ . The LM 339 is connected as a comparator to compare the unregulated input to the fixed reference input as shown in Fig. 2 [2].

**Table 1. Variation of unregulated voltage [ $V_{d.c}^+$ ] against public mains supply voltage [ $V_{a.c(in)}$ ] [2]**

$V_{a.c(in)}$	$V_{a.c(out)}$	$V_{d.c}^+$	$VR_2$
240	16.36	14.72	1.91
230	15.68	14.11	1.83
220	15.00	13.50	1.76
210	14.31	12.88	1.67
200	13.63	12.27	1.60
190	12.95	11.66	1.50
180	12.27	11.04	1.44
170	11.59	10.43	1.36

From Fig. 3, unregulated voltage is reduced to below 5V by the potential divider  $R_1$  and  $R_2$ . At our earlier stated mains supply threshold of 190V a.c input, we set = 1.5V.

We have that,

$$VR_2 = \frac{R_2}{R_1+R_2} V^+ \quad (4)$$

Where,  $VR_2$  is the drop across  $R_2$  and  $V^+$  is the unregulated voltage. Table 1 gives  $V^+$  of 11.66V at 190V a.c input. If we set  $R_1 = 100k\Omega$ ,  $R_2$  is obtained from equation (4).

The voltage drops across at different input a.c voltages are shown in Table 1. Potential divider  $R_1$  and  $R_2$  is for the fixed reference voltage. By setting the maximum adjustable reference to be 3.5V,  $R_4 = 2.5K\Omega$ , the voltage drop across  $R_3$  is thus given by;

$$VR_3 = \frac{R_3}{R_3+R_4} V^+ \quad (5)$$

The gain of the comparator unit is given by

$$A_0 = \frac{V_{out}}{V_{in}} \quad (6)$$

However, the usual value for open loop gain is  $\geq 20,000$  [2]. With this large gain,  $V_{out}$  will drop to  $V^+$  for any slight positive difference in voltage.

With a public mains supply input at above 1.5V, the D flip-flop receives a HIGH signal from the output of the comparator which makes the D flip-flop to energize the relay RLA1. With RLA1 energized, the installation (home or industrial load) is connected to the public mains supply. If however, the public mains supply input voltage goes below 1.5V, relay RLA1 is de-energized by the LOW signal sent to the D flip-flop thereby

connecting the installation to the alternative power supply.

### 2.3 Astable Oscillator Unit

Here, clock pulses are generated for the flip-flop which is required to for its operation in the SET and RESET modes. We use a 1KHz astable oscillator to clock the flip-flop via a 555 timer as shown in Fig. 3 [4].

The astable oscillator has two states, temporarily stable in each. The flow of digital pulses is continuous. The switching process between these states generates continuous rectangular waveforms with fast rise times [5].

The ON and OFF time of an astable oscillator is given as  $t_1$  and  $t_2$  and obtained from [3]:

$$t_1 = 1.1C_1 (R_5 + R_6) \tag{7}$$

$$t_2 = 0.693C_1R_6 \tag{8}$$

Where;

$C_1$  is oscillator capacitance

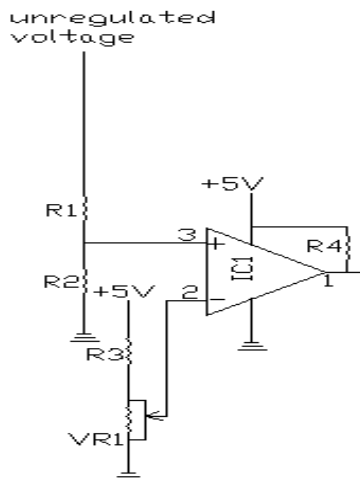


Fig. 2. Comparator unit

$R_5$  and  $R_6$  form a potential divider.  $R_6$  is obtained from the oscillator frequency which is given by:

$$F = \frac{1.44}{C_1(R_5+2R_6)} \tag{9}$$

For this work, we give  $R_6$  a preferred value of  $15k\Omega$ .  $t_1$  and  $t_2$  can be tuned to requirement

either by increasing or reducing the values of  $R_5$  and  $R_6$ .

### 2.4 The Switching Unit

To implement the switching process, we have a logic control and switching transistor circuit. The flip-flop does the logic control while the transistor undertakes the switching. We show in Fig. 5 how this is setup.

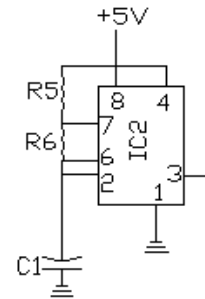


Fig. 3. Astable oscillator unit

#### 2.4.1 Logic control circuit

Through the D flip-flop, the system is instructed on when to energize (switch ON) or de-energize (switch OFF) the relays. For further description of how this flip-flop operates in the SET and RESET modes, please see Table 2 [6]. When the D flip-flop is clocked by the rising edge of the astable oscillator, a LOW signal is detected at the output of the comparator. This causes a data shift from the input (D) to output (Q) which switches off the relays. For a HIGH signal from the comparator triggered by the rising edge from the astable oscillator, the relays are energized and switched ON [7].

Table 2. D-flip flop's truth table

Mode	Input (D)	Clock	Output (Q) 0
Set	1	1	1
Reset	0	1	0

#### 2.4.2 Switching transistor circuit

A transistor and relays RLA1 and RLA2 makes up our switching circuit which is shown in Fig. 4. The relays are controlled based on the commands which the transistors from the D-flip-flop. The transistor operates in the class A mode as a switch [8]. When the D flip-flop is in SET mode, the relays are switched on. Reciprocally, the relays are switched off when the D flip-flop is

in RESET mode. In order to protect the relays from back EMF which is characteristic of inductive loads, diodes D5 and D6 are required [4].

To obtain the collector and base resistors, we use [9]:

$$V_{(reg)max}^+ = I_C K + V_{CE} \quad (10)$$

$$V_{(reg)min}^+ = I_B R_7 + V_{BE} \quad (11)$$

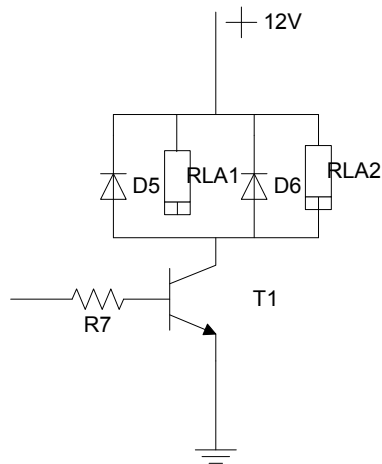


Fig. 4. Transistor and relays in circuit

While the base and collector currents are obtained from [2]:

$$h_{fe} = \frac{I_C}{I_B} \quad (12)$$

Where;

- $V_{CE}$  = Collector-Emitter voltage
- $I_C$  = Collector current
- $K$  = Collector resistance
- $V_{BE}$  = Base-Emitter voltage (silicon transistor)
- $I_B$  = Base current
- $R_7$  = Base resistor
- $h_{fe}$  = current gain
- $V_{(reg)max}^+$  = max regulated voltage
- $V_{(reg)min}^+$  = min regulated voltage

Equation 12 is the current gain equation.

Base resistor  $R_7$  is included in this unit to allow for perfect switching of the transistor in saturation. Relays RLA1 and RLA2 are replaced in Fig. 5 by K2. This is because relays RLA1 and

RLA2 are connected in parallel to form a resultant of  $400\Omega$  forming the collector/load resistor.

The complete circuit diagram of the switch is shown in Fig. 6.

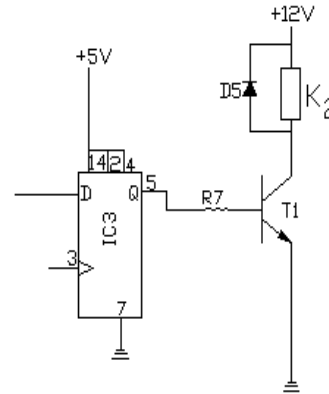


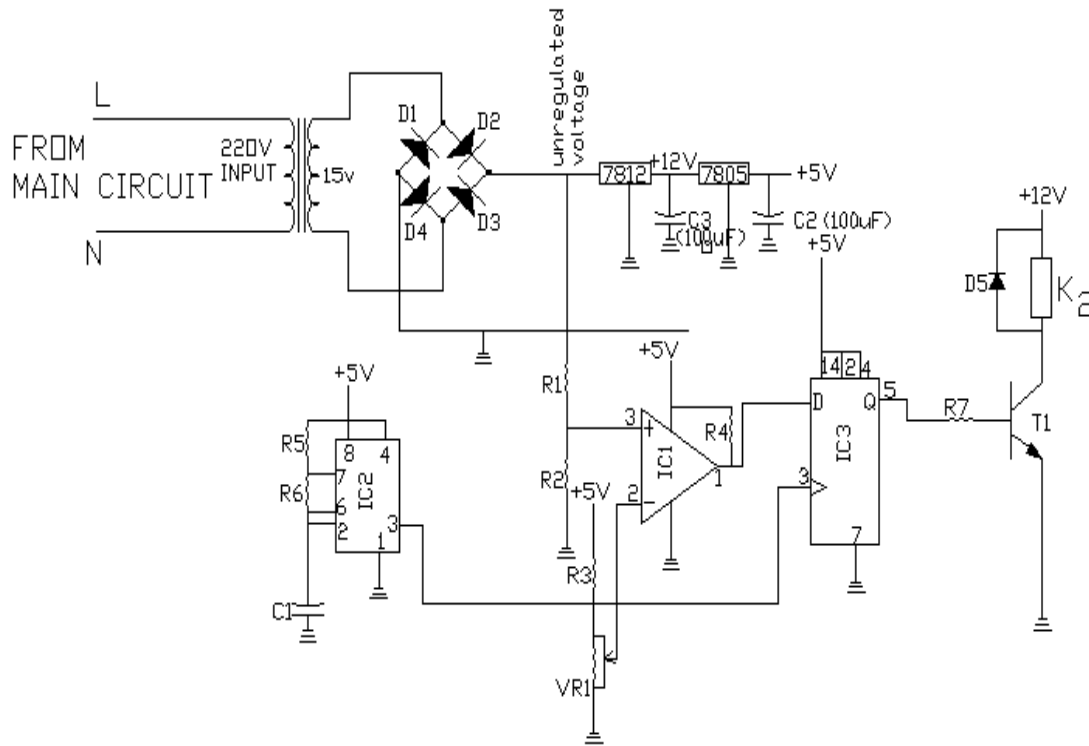
Fig. 5. Switching unit

### 3. PERFORMANCE EVALUATION

The changeover switch was tested with public mains supply source and a generator source to evaluate its performance. The contacts of the relays RLA1 and RLA2 were tested for continuity to identify the normally-open and normally-closed contacts in order to avoid wrong connection of the contacts. The public supply source was connected to the normally-open contact, while the generator source was connected to its normally-close contact.

A standing fan was connected to the output of the relay RLA1. When it was powered with public supply source, the relay switched instantly and the fan worked; when the public supply source was switched off, relay RLA1 returned to its normal state. The generator was then powered on by relay RLA2 and the fan worked; while the generator was working, the public supply source was suddenly switched on and the relay RLA1 immediately changed over to supply the fan with voltage from public mains supply, while the relay RLA2 disconnected the generator's ignition to switch it off.

From equation (7), the delay time of the changeover switch which is derived from the ON time of the astable oscillator given as  $t_1$  is calculated to be 0.88ms. This was tested and verified to be comparable.



**Fig. 6. Complete circuit diagram of the automatic change over switch**

As such, the changeover occurred in split seconds to prevent a noticeable power outage. Depending on specific design requirements, the delay time can be increased by increasing the resistances  $R_5$  and  $R_6$ . A test of this conformed to our expectation.

Reports consulted of previous projects [4,10-12] similar to this work have not been compared with each other. However, compared to [10] and [12] which state a delay time of 1.23 seconds and 5 seconds respectively measured during evaluation, we have implemented and tested in our performance evaluation delay times approximately 10 orders of magnitude less. The implication of the split-second changeover reported in this work is the readiness for this work to be adopted in installations with an energy mix variety and not only for generator source.

#### 4. CONCLUSION

The performance evaluation of the automatic changeover switch has shown that the switch is more reliable than manual change over switches. The switch was able to power up a standby generator and perform the load switching

between the mains public supply and the standby generator in a split second action to eliminate noticeable outage in power. Therefore it could be employed when two different sources of electrical power are supplied to an installation in which one of them should be connected to the installation at a given time. Its ability to change over in the twinkling of an eye is also commendable, thus eliminating the delay and human error associated with manual change over switches.

#### COMPETING INTERESTS

Authors have declared that no competing interests exist.

#### REFERENCES

1. Rocks G and Mazur G. Electric motor controls. New York, USA: American Technical Publisher; 1993.
2. Joseph SA, Odiba O, Ajise KA, Yakubu A. Development of a water-pump control unit with low voltage sensor. International Journal of Energy Engineering. 2015;5(2): 34-39.

3. Faissler WL. Introduction to modern electronics. New York, USA: Willey; 1991.
4. Ahmed MS, Mohammed AS, Agusiobo OB. Development of a single phase automatic change-over switch. Department of Electrical and Computer Engineering, Federal University of Technology Minna, Nigeria; 2006.
5. Mimms FM. Engineer's mini notebook, 555 Timer IC circuits. USA: Tandy Corporation; 1984.
6. Kuphaldt TR. Lessons in electric circuits – Digital. 2007;4.
7. Mimms FM. Digital logic circuits. USA: Tandy Corporation; 1986.
8. Theraja BL, Theraja AK. Electrical technology, 21<sup>st</sup> ed. New Delhi, India: Ranjendra Ravidia; 2002.
9. Menkiti AI, Abumere OE, Eze FC. Introduction to electronics, Ibadan, Nigeria: Spectrum Books Ltd.; 1996.
10. Ezema LS, Peter BU, Harris OO. Design of automatic change over switch with generator control mechanism. Academic Research International. 2012;3(3):125-130.
11. Agbetuyi AF, Adewale AA, Ogunluyi JO, Ogunleye DS. Design and construction of an automatic transfer switch for a single phase power generator. Department of Electrical and Information Engineering, Covenant University, Ota, Nigeria; 2015.
12. Kolo J. Design and construction of an automatic power changeover switch. Assumption University Journal of Technology. 2007;11(2):1-6.

© 2017 Onipede et al.; This is an Open Access article distributed under the terms of the Creative Commons Attribution License (<http://creativecommons.org/licenses/by/4.0>), which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

*Peer-review history:*  
*The peer review history for this paper can be accessed here:*  
<http://sciencedomain.org/review-history/19415>